

Appl. No. 10/627,165
Amdt. Dated July 13, 2006
Reply to Office Action of June 13, 2006

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CLAIMS

1. (Original) A method for planarizing a semiconductor wafer having an insulating layer on a surface thereof, the insulating layer comprising a field region and a plurality of features, the method comprising the steps of:

forming a barrier layer overlying at least the field region;

electrodepositing a layer comprising copper having a substantially planar upper surface overlying the barrier layer and filling the features in the insulating layer; and

polishing the layer comprising copper and the barrier layer on a single polishing pad to remove the layer comprising copper and the barrier layer from the field region.

2. (Original) The method of claim 1 wherein the step of polishing comprises the step of chemical mechanical planarizing the layer comprising copper and the layer of barrier material on a single soft polishing pad.

3. (Original) The method of claim 2 wherein the step of polishing comprises the step of chemical mechanical planarizing on a polishing pad having a hardness less than about 0.4 on the Shore D hardness scale.

4. (Original) The method of claim 2 wherein the step of chemical mechanical planarizing comprises the step of chemical mechanical planarizing in the presence of a polishing slurry having a copper:barrier layer selectivity of substantially 1:1.

5. (Original) The method of claim 2 wherein the step of chemical mechanical planarizing comprises the steps of:

chemical mechanical planarizing in the presence of a first slurry having a selectivity of copper:barrier greater than 1:1 to remove a first portion of the layer comprising copper; and

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chemical mechanical planarizing in the presence of a second slurry having a copper:barrier layer selectivity of substantially 1:1.

6. (Original) The method of claim 2 further comprising the steps of cleaning and buffing the field region on the single platen following the step of polishing.

7. (Original) The method of claim 1 wherein the step of forming a barrier layer comprises the step of forming a barrier layer overlying at least the field region comprising a low-k dielectric material insulating layer.

8. (Original) The method of claim 7 further comprising polishing the insulating layer on the single polishing pad to planarize the field region.

9. (Original) The method of claim 1 wherein the step of electrodepositing comprises the step of electrodepositing on a first platen of a multi-platen apparatus and the step of polishing comprises the step of polishing on a second platen of the multi-platen apparatus.

10. (Original) The method of claim 9 further comprising the step of cleaning the polished copper layer on a buff polishing station on a third platen of the multi-platen apparatus.

11. (Original) The method of claim 1 wherein the step of electrodepositing comprises the steps of:

forming a first seed layer comprising copper overlying the barrier layer; and

electrodepositing over the first seed layer a second layer comprising copper having a thickness less than about 300 nm as measured over the field region.

12. (Original) The method of claim 1 wherein the step of chemical mechanical planarizing comprises the step of chemical mechanical planarizing in the presence of a polishing slurry having a copper:barrier layer selectivity of substantially 1:1.

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13. (Original) A method for planarizing a semiconductor wafer having an insulating layer on a surface thereof, the insulating layer comprising a low-k dielectric material having a field region and a plurality of features, the method comprising the steps of:

forming a barrier layer overlying the insulating layer;

forming a seed layer comprising copper overlying and contacting the barrier layer;

electrochemical mechanical plating a layer comprising copper having a substantially planar upper surface overlying the seed layer and filling the features;

chemical mechanical polishing the layer comprising copper and the barrier layer on a single polishing platen to remove the layer comprising copper and the barrier layer from the field region.

14. (Original) The method of claim 13 wherein the step of chemical mechanical polishing comprises the step of electrochemical mechanical polishing.

15. (Original) The method of claim 13 wherein the step of chemical mechanical polishing comprises the steps of:

chemical mechanical polishing in the presence of a first slurry having a selectivity of copper:barrier layer greater than 1:1 to remove a first portion of the layer comprising copper; and

chemical mechanical polishing in the presence of a second slurry having a copper:barrier layer selectivity of substantially 1:1.

16. (Original) The method of claim 13 wherein the step of chemical mechanical polishing comprises the step of chemical mechanical polishing the layer comprising copper and the barrier layer on a soft polishing pad on the single polishing platen.

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17. (Original) A method for planarizing a work piece having a surface comprising a plurality of features and a field region, the method comprising the steps of:

forming a barrier layer overlying the field region and extending into the features;

electrodepositing a metal layer overlying the barrier layer and filling the features, the metal layer having a substantially planar upper surface over the features and the field region;

polishing the metal layer and the barrier layer on a single polishing pad to remove the metal layer and the barrier layer from the field region.

18. (Original) The method of claim 17 wherein the steps of electrodepositing and polishing comprise electrodepositing and polishing on two platens of a multi-platen electrochemical mechanical planarization apparatus.

19. (Original) The method of claim 17 wherein the steps of electrodepositing and polishing comprise the steps of:

electrodepositing a metal layer at a first platen of the multi-platen apparatus;

robotically moving the work piece to a second platen of the multi-platen apparatus; and

polishing the metal layer and the barrier layer at the second platen.

20. (Original) The method of claim 17 wherein the work piece is mounted on a work piece carrier and wherein the steps of electrodepositing and polishing comprise the steps of:

aligning the work piece carrier with respect to a first platen of the multi-platen apparatus;

electrodepositing a metal layer at the first platen;

aligning the work piece carrier with respect to a second platen of the multi-platen apparatus; and

polishing the metal layer and the barrier layer at the second platen.

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21. (Original) The method of claim 17 wherein the step of polishing the metal layer and the barrier layer on a single polishing pad comprises the step of polishing on a polishing pad having a hardness of less than about 0.4 on the Shore D hardness scale and at a pressure between the work piece and the polishing pad of less than about 2.5 psi.